



PATENT

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Aug. 21, 2006
Date

Joanne Bourguignon
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Philip J. Kuekes et al.

Application No.: 10/659,892

Filed: September 10, 2003

Title: DEFECT-TOLERANT AND FAULT-TOLERANT CIRCUIT
INTERCONNECTIONS

Examiner: Guy J. Lamarre

Art Unit: 2133

Docket No.: 200300253-1

Date: August 21, 2006

MAIL STOP AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSEINTRODUCTORY COMMENTS

Sir:

In response to the Office Action date April 21, 2006, please extend the time to respond one-month, from July 21, 2006 to August 21, 2006. The Petition for Extension of Time and requisite fee is enclosed. Please consider the following remarks: